

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Paul Gerard D'Arcy, *et al.*

Serial No.: N/A

Filed: Herewith

For: A REDUCTION CHECKSUM GENERATOR AND A METHOD OF  
CALCULATION THEREOFCommissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement. This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Form PTO-1449. Copies of the listed references are submitted herewith.

<u>U.S. Patent No.</u>	<u>Inventor</u>	<u>Date</u>
6,412,092 B1	Raghunath	June 25, 2002
6,324,670 B1	Henriksen	November 27, 2001
5,960,012	Spracklen	September 28, 1999

## References

C. Perkins; *Minimal Encapsulation Within IP*; RFC 2004; October 1996.

K. Bickerstaff, et al.; *Parallel Reduced Area Multipliers*; Journal of VLSI Signal Processing; Vol. 9; Kluwer Academic Publishers, Boston; 1995; pp. 181-192.

I. Koren; *Computer Arithmetic Algorithms*; Prentice-Hall, Inc.; 1993; pp. 71-72 and pp. 108-111.

J. Postel; *Internet Protocol*; RFC 791; University of Southern California, Information Sciences Institute; September 1981; pp. 1-45.

J. Postel; *Transmission Control Protocol*; RFC 793; University of Southern California, Information Sciences Institute; September 1981; pp. 1-85.

J. Postel; *User Datagram Protocol*; RFC 768; August 1980; pp. 1-3.

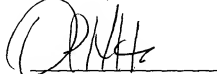
L. Dadda; *Some Schemes For Parallel Multipliers*; Alta Frequenza; Vol. 34; pp. 349-356; 1965.

C.S. Wallace; *A Suggestion For a Fast Multiplier*; IEEE Transactions on Electronic Computers; February 1964; pp. 14-17.

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement. The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. 08-2395.

Respectfully submitted,

HITT GAINES, P.C.

A handwritten signature in dark ink, appearing to read 'D. Hitt', is written over a horizontal line.

David H. Hitt  
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Date: JULY 15, 2003

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Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1

1

of

2

**Complete if Kn wn**

Application Number

N/A

Filing Date

Herewith

First Named Inventor

Paul Gerard D'Arcy

**Art Unit**

N/A

Examiner Name \_\_\_\_\_

N/A

Attorney Docket Number

D'ARCY 15-6-7

## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature	Date Considered
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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet **2** of **2**

### Complete if Known

Application Number	N/A
Filing Date	Herewith
First Named Inventor	Paul Gerard D'Arcy
Art Unit	N/A
Examiner Name	N/A
Attorney Docket Number	D'ARCY 15-6-7

### NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		C. Perkins; Minimal Encapsulation Within IP; RFC 2004; October 1996.	
		K. Bickerstaff, et al.; Parallel Reduced Area Multipliers; Journal of VLSI Signal Processing; Vol. 9; Kluwer Academic Publishers, Boston; 1995; pp. 181-192.	
		I. Koren; Computer Arithmetic Algorithms; Prentice-Hall, Inc.; 1993; pp. 71-72 and pp. 108-111.	
		J. Postel; Internet Protocol; RFC 791; University of Southern California, Information Sciences Institute; September 1981; pp. 1-45.	
		J. Postel; Transmission Control Protocol; RFC 793; University of Southern California, Information Sciences Institute; September 1981; pp. 1-85.	
		J. Postel; IUser Datagram Protocol; RFC 768; August 1980; pp. 1-3.	
		L. Dadda; Some Schemes For Parallel Multipliers; Alta Frequenza; Vol. 34; pp. 349-356; 1965.	
		C.S. Wallace; A Suggestion For a Fast Multiplier; IEEE Transactions on Electronic Computers; February 1964; pp. 14-17.	

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